

Fig. 1

```
graph TD
    201[201 Bin Based PDSynth Database] <--> 202[202 SPI Interface]
    202 --> 203[203 Interrupt Synthesis Placement Flow]
    203 --> 204[204 Invoke Callback]
    204 --> 205[205 Update Timing Invoke Script]
    205 --> 200[200 Apply Transforms with special drivers SCENARIO]
    200 --> 207[207 Select Transforms]
    207 --> 208[208 new line trained x forms]
    208 --> 209[209 modify synth place x forms]
    209 --> 200
    200 --> 202
```

Fig 2

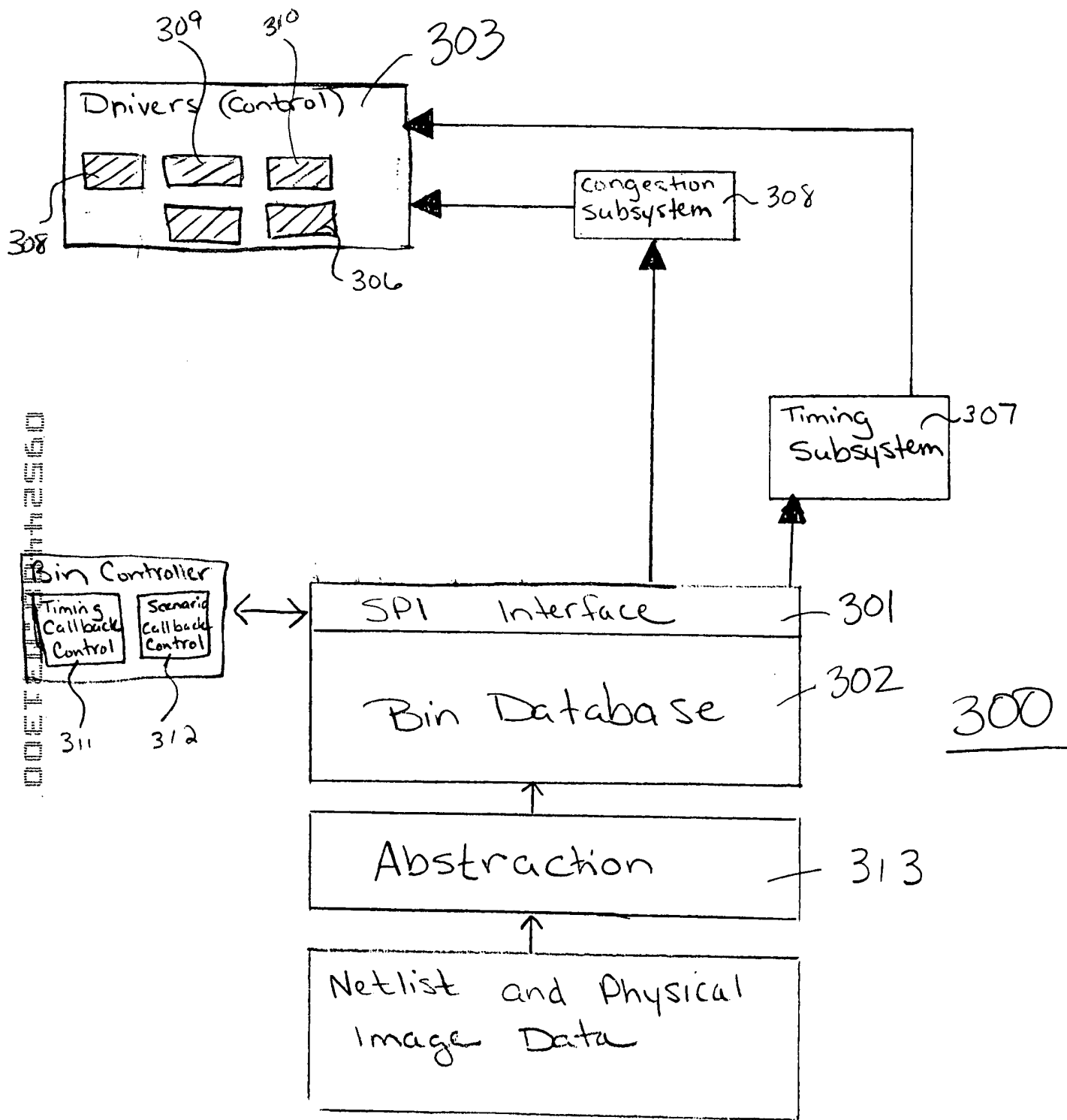


Fig. 3

```

BIN_DATA(
AREA CAPACITY
AREA USED
WIRE CAPACITY
WIRE USED
BLOCKAGE DATA

```



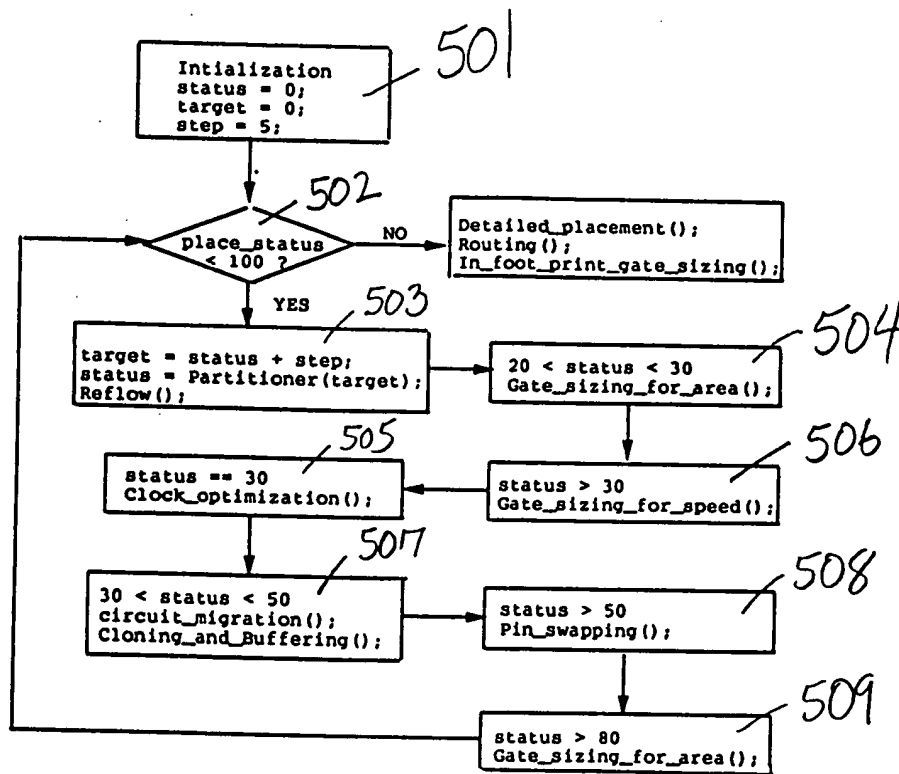


Fig. 5

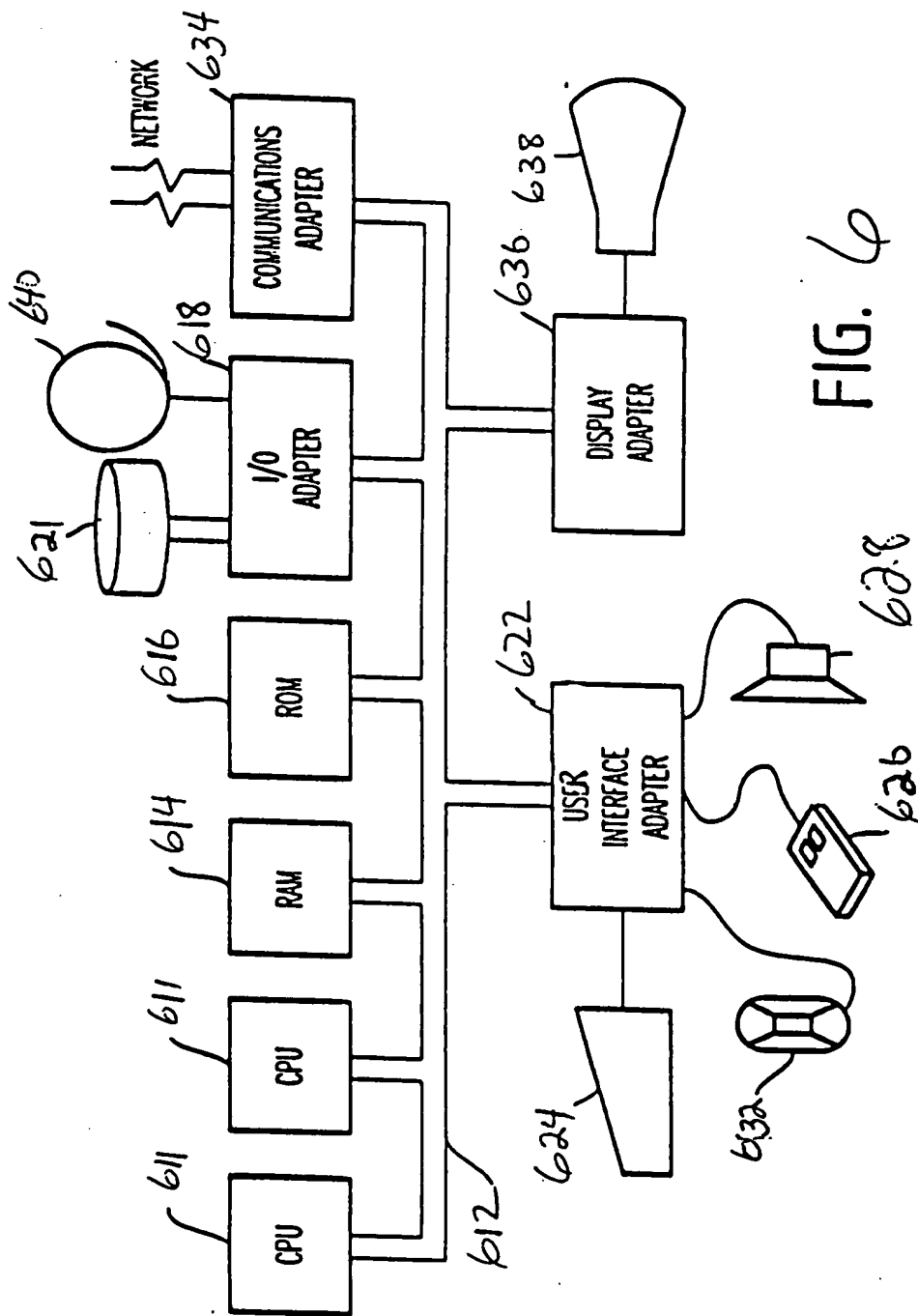


FIG. 6

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FIG 7

